ABSTRACT

Disclosed herein is a vertical alignment liquid crystal display device with high-speed response. This vertical alignment liquid crystal display device comprises: upper and lower substrates which are disposed opposite one another at the desired interval; a liquid crystal layer sandwiched between the upper and lower substrates and formed of liquid crystals having negative dielectric anisotropy; a resin layer which is applied on the inner surface of the lower substrate so as to cover a thin film transistor, the resin layer having a centipede-shaped protrusion formed on the surface thereof; a pixel electrode which is formed on the protrusion while being disposed all over a pixel region; a counter electrode which is formed on the inner surface of the upper substrate; vertical alignment films which are interposed between the pixel electrode and the liquid crystal layer and between the counter electrode and the liquid crystal layer, respectively; and polarizers which are attached on the outer surfaces of the upper and lower substrates, respectively, in such a manner that their polarizing axes cross each other.